Integration Manual –TmprlMon

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# Dependencies

## SWCs

|  |  |
| --- | --- |
| Module | Required Feature |
| None |  |

Note : Referencing the external components should be avoided in most cases. Only in unavoidable circumstance external components should be refered. Developer should track the references.

## Global Functions(Non RTE) to be provided to Integration Project

None

# Configuration

## Build Time Config

|  |  |  |
| --- | --- | --- |
| Modules | Notes |  |
| None |  |  |

## Configuration Files to be provided by Integration Project

**Sa\_TmprlMon\_Cfg.h** generated by Sa\_TmprlMon\_Cfg.h.tt

**Sa\_TmprlMon2\_Cfg.h** generated by Sa\_TmprlMon2\_Cfg.h.tt

### Da Vinci Parameter Configuration Changes

|  |  |  |
| --- | --- | --- |
| Parameter | Notes | SWC |
| TmprlMonGeneral/TmprlMonCPEnable | To enable the check point |  |
| TmprlMon2General/TmprlMon2CPEnable | To enable the check point |  |

### DaVinci Interrupt Configuration Changes

|  |  |  |  |
| --- | --- | --- | --- |
| ISR Name | VIM # | Priority Dependency | Notes |
| None |  |  |  |

### Manual Configuration Changes

|  |  |  |
| --- | --- | --- |
| Constant | Notes | SWC |
| None |  |  |

# Integration

## Required Global Data Inputs

None

## Required Global Data Outputs

None

## Specific Include Path present

No

# Runnable Scheduling

This section specifies the required runnable scheduling.

|  |  |  |
| --- | --- | --- |
| Init | Scheduling Requirements | Trigger |
| None | None | None |

|  |  |  |
| --- | --- | --- |
| Runnable | Scheduling Requirements | Trigger |
| TmprlMon\_Per1 | None | RTE (2ms) |
| TmprlMon\_Per2 | None | RTE (2ms)  not in Mode(s) <DISABLE, OPERATE, OFF> |
| TmprlMon\_Per3 | None | RTE (10ms)  not in Mode(s) <WARMINIT, OFF> |
| TmprlMon\_Trns1 | None | RTE on entering WARMINIT mode |
| TmprlMon\_Trns2 | None | RTE on entering DISABLE mode |
| TmprlMon2\_Per1 | None | RTE (2ms) |

**.**

# Memory Mapping

## Mapping

|  |  |  |
| --- | --- | --- |
| Memory Section | Contents | Notes |
| TMPRLMON\_START\_SEC\_CONST\_UNSPECIFIED |  |  |
| TMPRLMON\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |  |  |
| TMPRLMON\_START\_SEC\_VAR\_CLEARED\_32 |  |  |
| TMPRLMON\_START\_SEC\_VAR\_CLEARED\_8 |  |  |
| TMPRLMON\_START\_SEC\_VAR\_CLEARED\_BOOLEAN |  |  |
| TMPRLMON\_START\_SEC\_VAR\_CLEARED\_16 |  |  |
| RTE\_START\_SEC\_SA\_TMPRLMON2\_APPL\_CODE |  |  |
| RTE\_START\_SEC\_SA\_TMPRLMON\_APPL\_CODE |  |  |

\* Each …START\_SEC… constant is terminated by a …STOP\_SEC… constant as specified in the AUTOSAR Memory Mapping requirements.

## Usage

|  |  |  |
| --- | --- | --- |
| Feature | RAM | ROM |
| None |  |  |

Table 1: ARM Cortex R4 Memory Usage

## Non RTE NvM Blocks

|  |
| --- |
| Block Name |
| None |

Note : Size of the NVM block if configured in developer

## RTE NvM Blocks

|  |
| --- |
| Block Name |
| None |

Note : Size of the NVM block if configured in developer

# Compiler Settings

## Preprocessor MACRO

None

## Optimization Settings

None

# Revision Control Log

|  |  |  |  |
| --- | --- | --- | --- |
| **Rev #** | **Change Description** | **Date** | **Author** |
| 1 | Initial version | 3-Feb-14 | Rijvi |